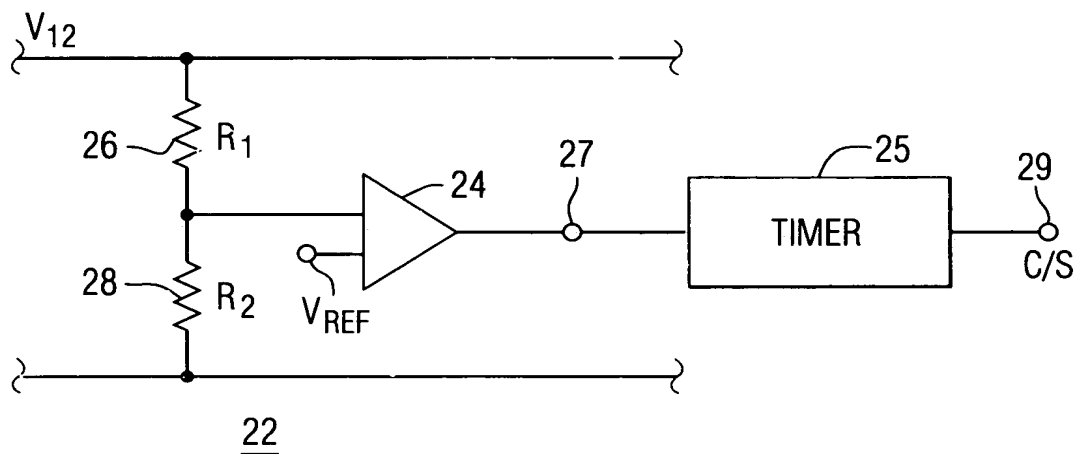
**FIG. 1****FIG. 2**

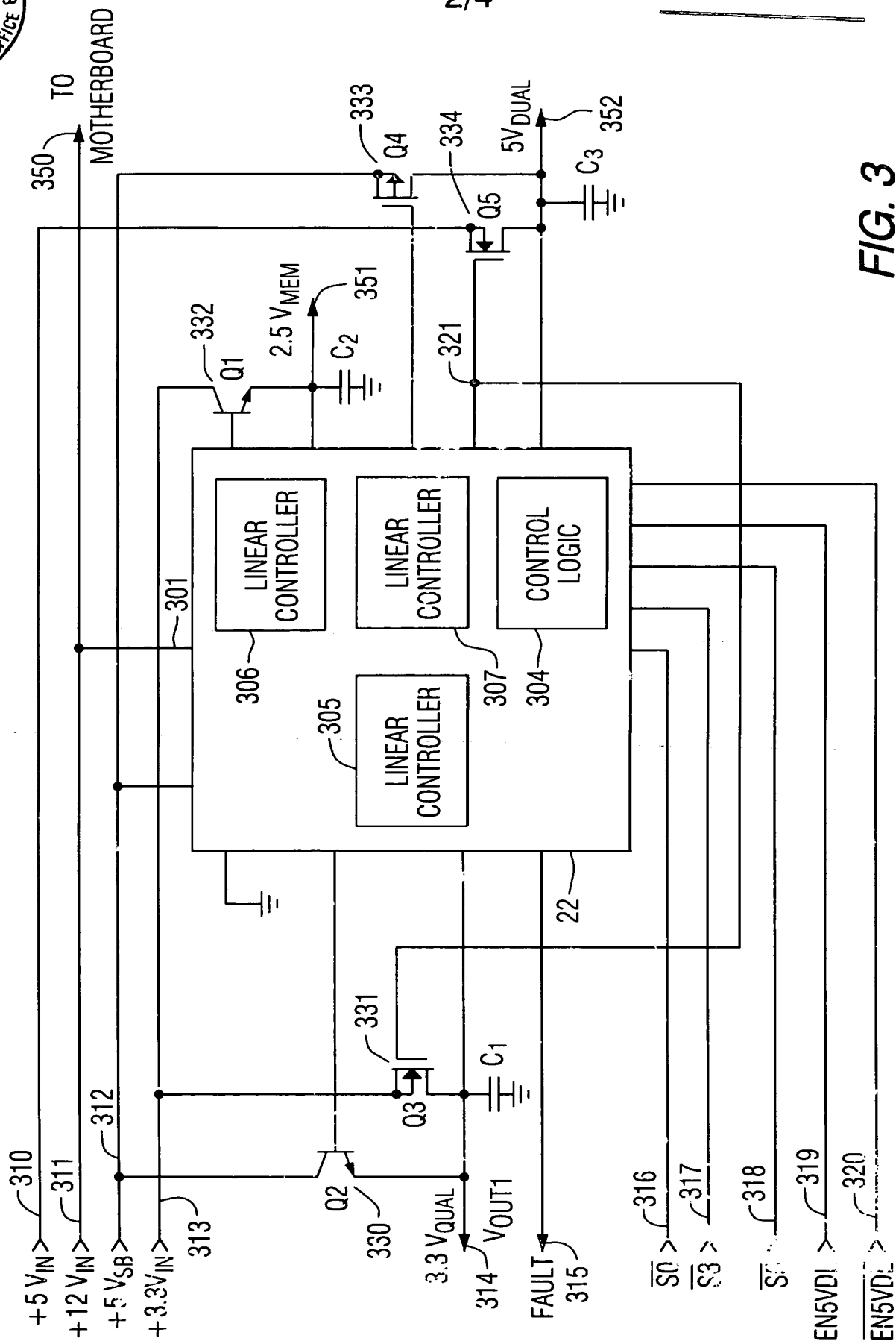
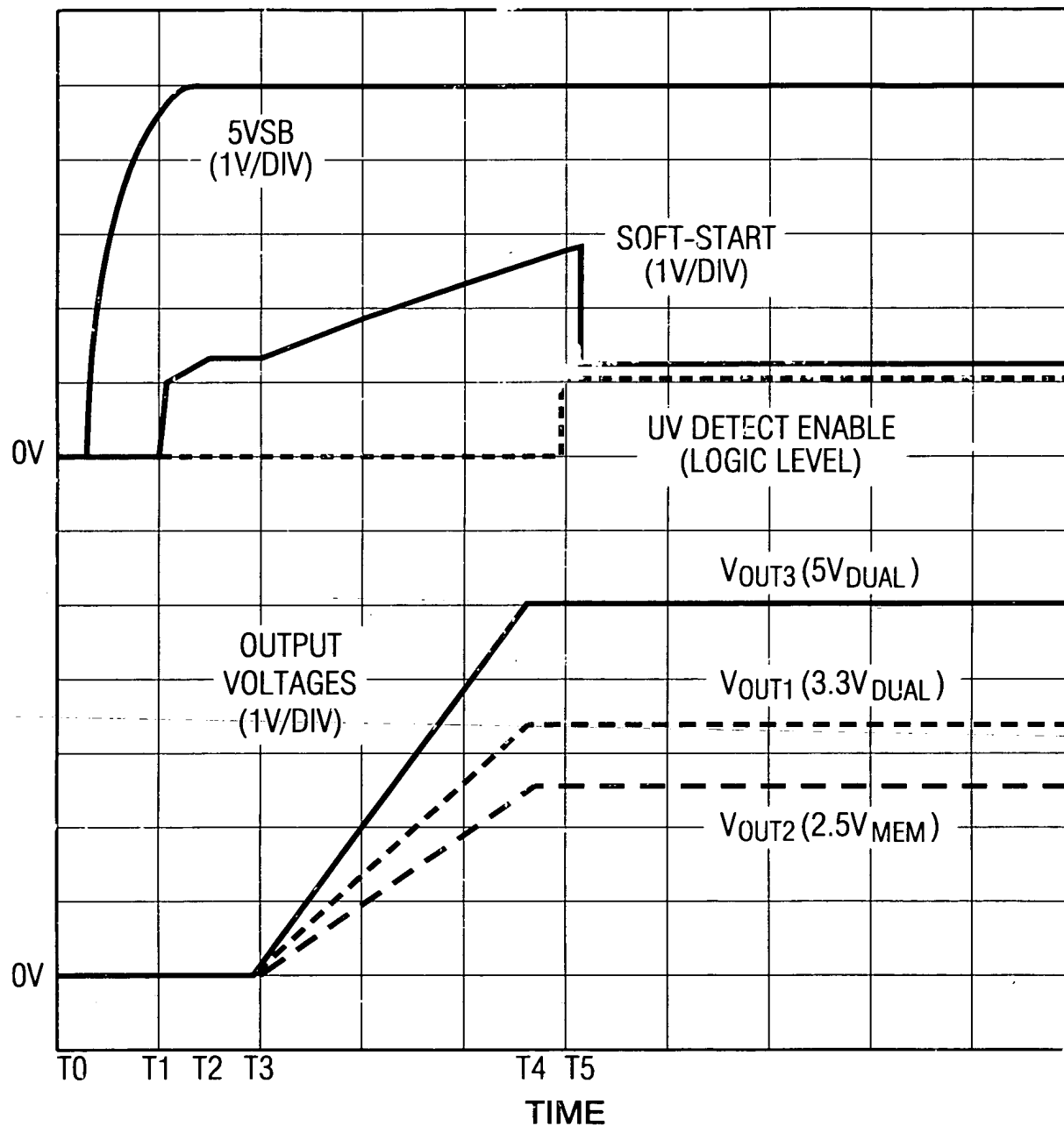
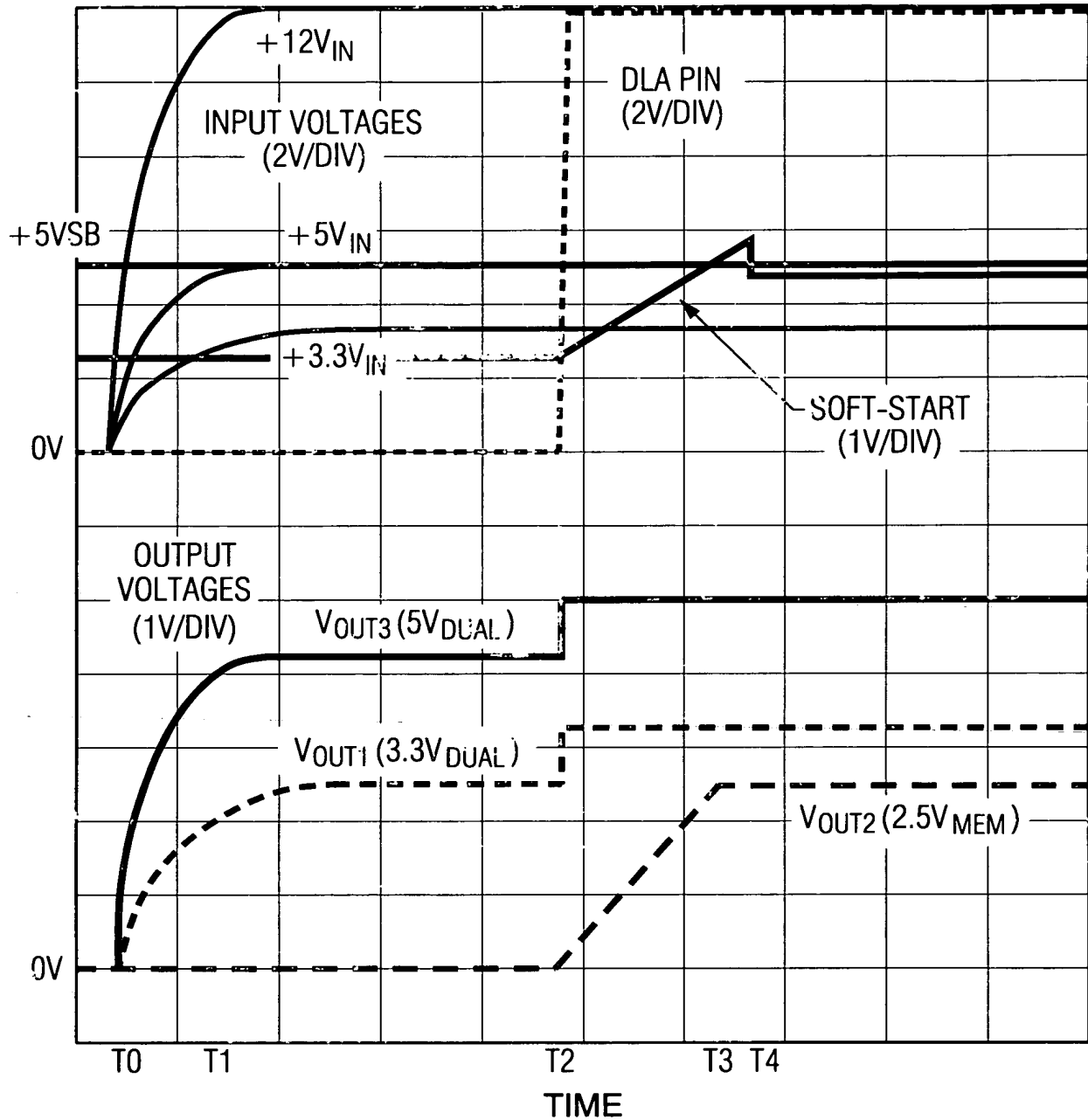


FIG. 3



SOFT-START INTERVAL IN A SLEEP STATE
(ALL OUTPUTS ENABLED)

FIG. 4



SOFT-START INTERVAL IN AN ACTIVE STATE

FIG. 5